

METHOD FOR PACKAGING A SEMICONDUCTOR DEVICE

Field of the Invention

5 The present invention relates to a method for packaging a semiconductor device; and, more particularly, to a method for packaging a semiconductor device including a substrate having grooves thereon and a heat sink attached thereto to improve the heat dissipating efficiency.

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Background of the Invention

Fig. 1 illustrates a sectional view of a BGA package fabricated in accordance with a method of the prior art. As shown in Fig. 1, in accordance with the prior art, a chip 106 is attached to a substrate 100 through an adhesive 102. Thereafter, the chip 106 is interconnected with the substrate 100 through a gold wire 104. Then, the chip 106 and the gold wire 104 are encapsulated and sealed by using an epoxy molding compound 108. Solder balls 110 are then attached to the substrate 100 and subject to a reflow process. Subsequently, the substrate is sawed to singulate individual packages, by which a fabrication of the BGA package is completed.

25 However, the BGA package of the prior art has problems that it is hard to miniaturize the package and the package

has insufficient heat transfer capability.

Summary of the Invention

5 It is, therefore, an object of the present invention to provide a method for packaging a semiconductor device including a substrate having grooves thereon and a heat sink attached thereto to improve the heat dissipating efficiency.

10 In accordance with a preferred embodiment of the present invention, there is provided a method for packaging a semiconductor device, including the steps of: (a) forming a plurality of grooves on an upper surface of a substrate; (b) attaching a heat sink on the substrate; (c) attaching a chip to the substrate by using an adhesive; (d) wire-bonding the substrate and the chip; and (f) encapsulating and sealing the substrate and the chip by using an epoxy molding compound.

Brief Description of the Drawings

20 The above and other object and features of the present invention will become apparent from the following description of preferred embodiments given in conjunction with the accompanying drawings, in which:

25 Fig. 1 describes a sectional view of a BGA package fabricated by using a method in accordance with the prior

art; and

Fig. 2 depicts a sectional view of a BGA package fabricated by using a method in accordance with a preferred embodiment of the present invention.

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Detailed Description of the Preferred Embodiment

Fig. 2 illustrates a sectional view of a BGA package fabricated by using a method in accordance with a preferred 10 embodiment of the present invention. In the following, the method of the present invention will be described in detail with reference to Fig. 2.

As shown in Fig. 2, first, grooves 202 are formed on an upper surface of a substrate 200. Then, a heat sink 204 for improving heat dissipation efficiency is attached to the 15 substrate 200. Herein, the grooves formed on the substrate 200 enlarges an area where the substrate 200 and an epoxy molding compound 206 to be attached thereto contact with each other, which improves the operational reliability of 20 the semiconductor device. Further, the heat sink 204 attached to the substrate 200 facilitates a dissipation of heat generated during the operation of the semiconductor device, which prevents failure of the semiconductor device.

Thereafter, a chip 208 is attached to the substrate 200 by using an adhesive 210. The chip 208 and the substrate 200 is wire-bonded by using a gold wire 212 and

then encapsulated and sealed by using an epoxy molding compound 206. Subsequently, solder balls 214 are attached to the substrate 200 and subject to a reflow process. Finally, the substrate 200 is sawed to singulate individual
5 packages.

As described above, in accordance with present invention, the grooves formed on the substrate enlarges an area where the substrate and the epoxy molding compound attached thereto contact with each other, which improves the
10 operational reliability of the semiconductor device. Further, the heat sink attached to the substrate facilitates a dissipation of heat generated during the operation of the semiconductor device, which prevents failure of the semiconductor device.

15 While the invention has been shown and described with respect to the preferred embodiments, it will be understood by those skilled in the art that various changes and modifications may be made without departing from the spirit and scope of the invention as defined in the following
20 claims.